

10/6/06, 021

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<input type="checkbox"/>	L11	(high near4 speed near4 (clock\$ or data) near3 (detect\$3 or recover\$3 or sampling\$)) and (phase near3 detect\$3) and (parallel near3 sampl\$4)	32
<input type="checkbox"/>	L10	(Ghz near4 (clock\$ or data) near3 (detect\$3 or recover\$3 or sampling\$)) same (phase near3 detect\$3)	15
<input type="checkbox"/>	L9	( 40Ghz near4 (clock\$ or data) near3 (detect\$3 or recover\$3 or sampling\$)) same (phase near3 detect\$3)	0
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<input type="checkbox"/>	L6	(high near4 speed near4 (clock\$ or data) near3 (detect\$3 or recover\$3 or sampling\$)) same (phase near3 detect\$3 near4 mult\$5 near4 phase\$)	1
<input type="checkbox"/>	L5	L4 and l3	126
<input type="checkbox"/>	L4	@ad<=20030207	23740862
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<input type="checkbox"/>	L2	(high near4 speed near4 (clock\$ or data) near3 (detect\$3 or recover\$3 or sampling\$))	2654
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#5	((40 <near/1> ghz <near/2> (clock* <or> data*) <near/2> recover* )<in>metadata)
#6	((40 <near/1> ghz <and> ((clock* <or> data*) <near/2> recover* )<in>metadata)
#7	((40 <near/1> ghz <and> ((clock* <or> data*) <near/2> recover* )<in>metadata)
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